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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	[GATE STRUCTURE FORMING METHOD OF FIELD EFFECT TRANSISTOR]				
Application Number :					
Date :					
First Named Applicant:	Mr. Chin-Tsai Hsu				
Confirmation Number:					
Attorney Docket Number:	11248-US-PA				
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td></tr><tr><td>Dr. Belinda Lee Registered Number: 46863</td><td>/Belinda Lee/</td></tr></table>		Submitted By:	Elec. Sign.	Dr. Belinda Lee Registered Number: 46863	/Belinda Lee/
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Documents being submitted:	Files
us-fee-sheet	11248-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	11248-usassn.xml us-assignment.xsl us-assignment.dtd 11248assignment2.tif 11248assignment001.tif
us-request	11248-usrequ.xml us-request.dtd us-request.xsl 11248declaration2.tif 11248declaration01.tif
us-declaration	11248-trans.xml
us-declaration	us-application-body.xsl application-body.dtd
application-body	wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 11248us0001.tif 11248us0002.tif 11248us0003.tif 11248us0004.tif 11248us0005.tif 11248us0006.tif 11248us0007.tif 11248us0008.tif

Comments

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